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APPLICANTS

Kiminori Watanabe, Kawasaki-shi, JAPAN;

Keisuke Matsuoka, Fukuoka-shi, JAPAN;
Takao Ito, Tokyo, JAPAN;**** CONTINUING DATA *******This application is a DIV of 10/277,744 10/23/2002 PAT 6,707,104
which is a DIV of 09/746,223 12/26/2000 PAT 6,489,653**** FOREIGN APPLICATIONS *******JAPAN 11-371568 12/27/1999
JAPAN 2000-205070 07/06/2000**IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 2	INDEPENDENT CLAIMS 1
Verified and Acknowledged Examiner's Signature 	Initials 				

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TITLE

Lateral high-breakdown-voltage transistor

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